INCH-POUND
MIL-PRF-914/5A
3 July 1997
SUPERSEDING
MIL-R-914/5
12 February 1991

#### PERFORMANCE SPECIFICATION

RESISTOR NETWORK, FIXED, FILM, SURFACE MOUNT, 16-PIN, LEADLESS CHIP CARRIER, NONESTABLISHED RELIABILITY AND ESTABLISHED RELIABILITY, STYLE RNS050

This specification is approved for use by all Departments and Agencies of the Department of Defense.

#### 1. SCOPE

- 1.1 <u>Scope</u>. This specification covers the associated requirements for RNS050 resistor networks. Resistors covered by this specification are considered a monolithic design or wire bond and die construction. The established reliability (ER) and nonestablished reliability (non-ER) styles are available in all characteristics and resistance tolerances.
- 1.2 Part or Identifying Number (PIN) (see 6.4). Resistor networks covered by this specification are identified by a PIN which consists of the basic number of this specification and a coded number. The military PIN is in the following form:

The coded number is derived in accordance with MIL-PRF-914.

1.2.1 <u>Schematic</u>. The schematic of the resistor network is identified by a single letter in accordance with figure 1. The resistor element  $R_{ref}$  is the reference resistor element used in determining the ratio accuracy (when applicable).

Beneficial comment (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be address to: US Army Communications - Electronics Command, ATTN: AMSEL-LC-LEO-E-EP, Fort Monmouth, NJ 07703-5023 by using the Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

AMSC N/A FSC 5905

<u>DISTRIBUTION STATEMENT A</u>. Approved for public release; distribution is unlimited.

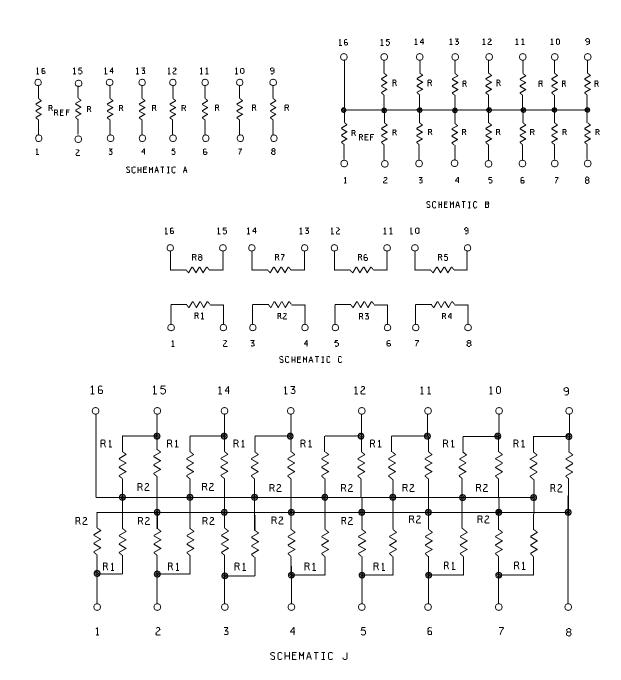


FIGURE 1. Schematics.

#### 2. APPLICABLE DOCUMENTS

2.1 <u>General</u>. The documents listed in this section are specified in sections 3 and 4 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document user are cautioned that they must meet all specified requirements documents cited in sections 3 and 4 of this specification, whether or not they are listed.

### 2.2 Government documents.

2.2.1 <u>Specification, standards, and handbooks</u>. The following specifications, standards and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation (see 6.2).

#### **SPECIFICATIONS**

### **DEPARTMENT OF DEFENSE**

MIL-PRF-914 - Resistor Networks, Fixed, Film, Surface Mount, Nonestablished Reliability and Established Reliability, General Specification for.

(Unless otherwise indicated, copies of federal and military specifications, standards, and handbooks are available from the Defense Printing Service Detachment Office, Bldg. 4D (Customer Service), 700 Robbins Avenue, Philadelphia PA 19111-5094.)

2.3 <u>Order of precedence</u>. In the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

### 3. REQUIREMENTS

- 3.1 <u>General</u>. The requirements for acquiring the product described herein shall consist of this document and MIL-PRF-914.
- 3.2 <u>Interface and physical dimension</u>. The resistor networks shall meet the interface and physical dimensions specified on figure 2.
- 3.3 <u>Power rating</u>. The power ratings for schematic A, schematic B, schematic C, and schematic J shall be as specified in table I.
- 3.4 <u>Power conditioning</u>. The power applied for power conditioning for schematic A, schematic B, schematic C, and schematic J shall be one and one-half times rated power.
  - 3.5 <u>Temperature range</u>. The operating temperature range shall be from -55°C to +125°C.
  - 3.6 Voltage rating. The maximum voltage rating shall be 50 V dc.
  - 3.7 Resistance range. The resistance range shall be 10 ohms to 2.2 megohms.

3.8 <u>Standard resistance values</u>. The standard resistance values and the resistance designator for the "S" schematic are as specified in table II.

TABLE I. Power ratings.

Schematics	Characte K and		Characteristics C, R, H, and V		
	Element	Network	Element	Network	
	(watts)	(watts)	(watts)	(watts)	
A	0.100	0.800	0.050	0.400	
B	0.055	0.800	0.025	0.375	
C	0.050	0.800	0.100	0.400	
J	0.030	0.800	0.015	0.400	

TABLE II. Standard resistance values.

Resistance designator	R1 (ohms)	R2 (ohms)	Resistance designator	R1 (ohms)	R2 (ohms)
A001	82	130	A010	330	470
A002	120	200	A011	330	680
A003	130	210	A012	1.5 k	3.3 k
A004	160	260	A013	3 k	6.2 k
A005	180	240	A014	180	270
A006	180	390	A015	270	270
A007	220	270	A016	560	560
A008	220	330	A017	560	1.2 k
A009	330	390	A018	620	2.7 k

- 3.9 Resistance tolerance. Resistors are available in (B)  $\pm 0.1$  percent, (D)  $\pm 0.5$  percent, (F)  $\pm 1.0$  percent, (G)  $\pm 2$  percent, (J)  $\pm 5$  percent resistance tolerances.
- 3.10 <u>Termination</u>. Resistors are available in termination A, termination D, and termination G for configuration A and termination B, termination E, and termination H for configuration B.
- 3.11 <u>Marking</u>. Due to the size limitations, this style resistor shall be marked with the following minimum information, in the order as shown:
  - 042K Date code, characteristic.
  - 10K0 Coded resistance value.
  - FAMJ Tolerance, schematic, failure rate, JAN marking.

### 4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection shall be in accordance with MIL-PRF-914.

4.2 "J" schematic tests. For tests on "J" schematic, the dc resistance measurements, resistance temperature characteristic measurement, and short time overload application shall be made between each terminal and applicable common terminal without compensation for the shunt circuits to determine the acceptability to section 3 requirements.

# Configuration A

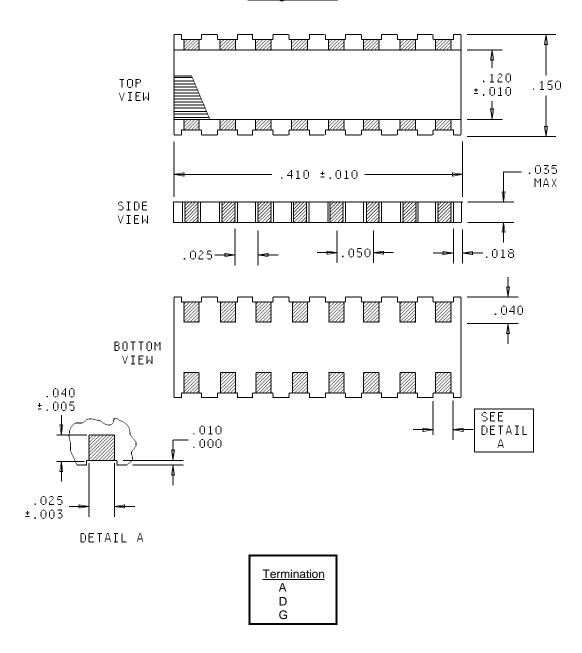
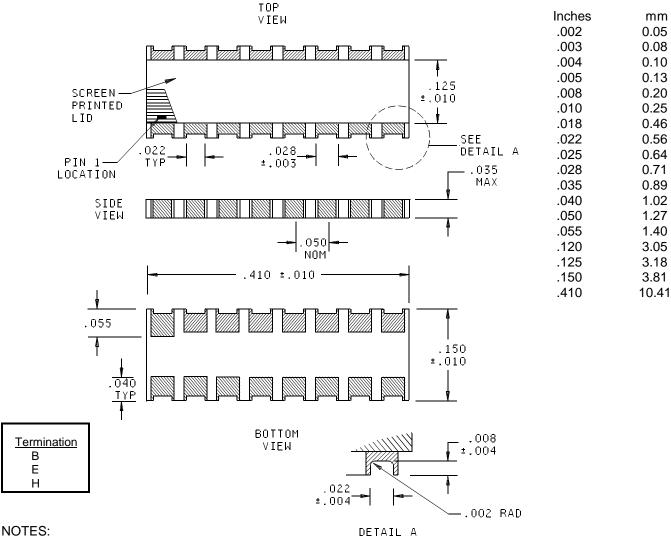


FIGURE 2. Leadless chip carrier.

## Configuration B



- 1. Dimensions are in inches.
- 2. Metric equivalents are given for general information only.
- 3. Unless otherwise specified, tolerances are ±.008 (0.20 mm).
- 4. Pin 1 locator shall be a dot, stripe, notch, or numeral adjacent to pin number 1, in the shaded area.
- 5. The picturization of the styles above is given as representative of the envelope of the item. Slight deviations from the outline shown, which are contained within the envelope and do not alter the functional aspect of the device, are acceptable.

FIGURE 2. Leadless chip carrier - Continued.

#### 5. PACKAGING

5.1 <u>Packaging</u>. For acquisition purposes, packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of material is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department or Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

#### 6. NOTES

(This section contains information of a general nature that may be helpful, but is not mandatory.)

- 6.1 <u>Intended use</u>. Resistor networks are used in surface mount applications where space is a major concern.
  - 6.2 <u>Acquisition requirements</u>. Acquisition documents must specify the following:
    - a. Title, number, and date of the specification, and the complete PIN.
    - b. Issue of DoDISS to be cited in the solicitation, and if required, the specific issue of the individual documents referenced (see 2.1).
    - c. Packaging requirements (see 5.1).
    - d. Allowable substitution.
- 6.3 <u>Soldering</u>. Extreme care should be taken when soldering these resistors. Prolonged exposure to high temperatures can damage these devices.
- 6.4 <u>PIN (see 1.2.)</u> PIN is a new term encompassing terms previously used in specifications such as part number, type designator, identification number, etc.
  - 6.5 Subject term (key word) listing.

Monolithic design
Wire bond and die construction

6.6 <u>Changes from previous issue</u>. Marginal notations are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians: Army - CR Navy - EC Air Force - 85

Review activities:

Army - AR, AT, AV, CR4 Navy - AS, CG, MC, OS Air Force - 17, 19

Preparing activity: Army - CR

Agent: DLA - CC

(Project 5905-1444-06)

# STANDARDIZATION DOCUMENT IMPROVEMENT PROPOSAL

# **INSTRUCTIONS**

- The preparing activity must complete blocks 1, 2, 3, and 8. In block 1, both the document number and revision letter should be given.
- The submitter of this form must complete blocks 4, 5, 6, and 7. 2.
- The preparing activity must provide a reply within 30 days from receipt of the form.

NOTE: This form may not be used to reque contracts. Comments submitted on this form or to amend contractual requirements.				
I RECOMMEND A CHANGE:	1. DOCUMENT NUM MIL-PRF-914/5A	BER		IENT DATE (YYMMDD) -JUL-1997
3. DOCUMENT TITLE RESISTOR NETWORK, F RELIABILITY AND ESTABLISHED RELIABIL		MOUNT, 16-PIN, LEADLE	SS CHIP CA	RRIER, NONESTABLISHED
4. NATURE OF CHANGE (Identify paragraph numbers)  5. REASON FOR RECOMMENDATION	ber and include propose	d rewrite, if possible. Attach	n extra sheets	as needed.)
6. SUBMITTER	· · · · · · · · · · · · · · · · · · ·			
a. NAME (Last, First, Middle Initial)		b. ORGANIZATION		
c. ADDRESS (Include Zip Code)		d. TELEPHONE (Include at 1) Commercial (2) AUTOVON	Area Code)	7.DATE SUBMITTED (YYMMDD)
8. PREPARING ACTIVITY		(if applicable)		
NAME     US Army Communications-Electronics Cor	mmand	b. TELEPHONE Include A (1) Commercial (908)427-3441	Area Code)	(2) AUTOVON 987-3441
c. ADDRESS (Include Zip Code) ATTN: AMSEL-LC-LEO-E-EP Fort Monmouth, NJ 07703-5023		IF YOU DO NOT RECEIVE A REPLY WITHIN 45 DAYS, CONTACT: DEFENSE QUALITY AND STANDARDIZATION OFFICE 5203 Leesburg Pike, Suite 1403, Falls Church, VA 22401-3466 Telephone (703) 756-2340 AUTOVON 289-2340		